

記号	借地権割合	記号	借地権割合
A	90%	E	50%
B	80%	F	40%
C	70%	G	30%
D	60%		

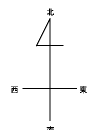


Diagram illustrating memory layout for the instruction `LD R0, #0x00000000` (Instruction 08201):

- Instruction 08201 is located at memory address 08201.
- The instruction is 4 bytes long, spanning from address 08201 to 08206.
- The instruction is stored in memory as: `00 00 00 00` (hexadecimal).
- The instruction is also shown in binary: `00000000 00000000`.

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